

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	0	Rhelimi-Alain.in. and @pd>20030824	USPAT; US-PGPUB; EPO; JPO	2004/03/29 14:58			0
2	BRS	L2	364	(713/501.ccls. 235/492.ccls. 902/26.ccls. 713/500.ccls. 710/302.ccls. 710/301.ccls. 235/441.ccls.) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 14:59			0
3	BRS	L3	44	((smart chip ic circuit memory) adj1 card\$1) same ((USB (universal adj serial adj bus)) near5 (contact\$1 terminal\$1 pad\$1 input\$4 output\$4)) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:24			0
4	BRS	L5	1	((USB (universal adj serial adj bus)) near5 ("7816")) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:26			0
5	BRS	L6	0	((USB (universal adj serial adj bus)) near5 ("7816")) and @pd>20030824	EPO; JPO; DERWENT; IBM_TDB	2004/03/29 16:27			0
6	BRS	L7	2	((USB (universal adj serial adj bus)) same ("7816")) and @pd>20030824	EPO; JPO; DERWENT; IBM_TDB	2004/03/29 16:27			0
7	BRS	L8	3	((smart chip ic circuit memory) adj1 card\$1) same ((USB (universal adj serial adj bus)) near5 (contact\$1 terminal\$1 pad\$1 input\$4 output\$4)) and @pd>20030824	EPO; JPO; DERWENT; IBM_TDB	2004/03/29 16:29			0
8	BRS	L9	18	((smart chip ic circuit memory) adj1 card\$1) same ((multiple plural\$3 several two three four many) near5 ((input near3 output) IO "I/O" "i/o") near5 (terminal\$1 contact\$1 line\$1 pin\$1)) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:32			0
9	BRS	L10	3	((smart chip ic circuit memory) adj1 card\$1) same ((multiple plural\$3 several two three four many) near5 ((input near3 output) IO "I/O" "i/o") near5 (terminal\$1 contact\$1 line\$1 pin\$1)) and @pd>20030824	EPO; JPO; DERWENT; IBM_TDB	2004/03/29 16:43			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
10	BRS	L11	5	(usb (universal adj serial adj bus)) near5 (mbs (megabits near2 second)) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:46			0
11	BRS	L12	2	(usb (universal adj serial adj bus)) near5 external\$2 near5 (clock clk) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:49			0
12	BRS	L13	1	(usb (universal adj serial adj bus)) near5 (clock\$3 clk) near5 (internal\$2 within) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:50			0
13	BRS	L14	4	(usb (universal adj serial adj bus)) near5 requir\$3 near5 (clock\$3 clk) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:51			0
14	BRS	L15	1	((smart chip ic circuit) adj1 card) near5 internal\$2 near5 (generat\$3 creat\$3) near5 (clock\$3 clk) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:55			0
15	BRS	L16	20	(usb (universal adj serial adj bus)) near5 (sequence order) near5 (connect\$3 contact\$3) and @pd>20030824	USPAT; US-PGPUB	2004/03/29 16:55			0